## Ten Week Session

**EENG 2610  CIRCUIT ANALYSIS**
001 (11629) CRE 3.0 MW 09:00 am-10:50 am NTDP B217 Bailey C

**EENG 2710  LOGIC DESIGN**
001 (9025) CRE 3.0 MW 12:00 pm-01:50 pm NTDP B217 Bailey C

**EENG 2980  EXPERIMENTAL COURSE**
001 (13245) CRE V T 05:29 pm-08:20 pm NTDP B207 Bailey C  
Course Topic: CIRCUIT ANALYSIS LAB
003 (13246) CRE V R 05:29 pm-08:20 pm NTDP B207 Bailey C  
Course Topic: DIGITAL LOGIC DESIGN LAB

**EENG 3510  ELECTRONICS I**
001 (12765) CRE 3.0 MW 03:00 pm-04:50 pm NTDP B217 Bailey C

**EENG 4920  COOP IN EE**
001 (11634) CRE V  
REstricted Course. Approval Required. Contact Department.

**EENG 4980  EXPERIMENTAL COURSE**
001 (13381) CRE 1.0 T 12:59 pm-03:50 pm NTDP B207 Bailey C

**EENG 5890  STUDY OF TOPICS IN EE**
001 (9030) CRE V  
REstricted Course. Approval Required. Contact Department.
002 (9431) CRE V  
REstricted Course. Approval Required. Contact Department.
003 (8935) CRE V  
REstricted Course. Approval Required. Contact Department.
004 (9031) CRE V  
REstricted Course. Approval Required. Contact Department.
005 (11630) CRE V  
REstricted Course. Approval Required. Contact Department.
006 (9275) CRE V  
REstricted Course. Approval Required. Contact Department.
007 (9276) CRE V  
REstricted Course. Approval Required. Contact Department.
008 (11631) CRE V  
REstricted Course. Approval Required. Contact Department.
009 (11632) CRE V  
REstricted Course. Approval Required. Contact Department.

**EENG 5932  INTERNSHIP**
001 (9347) CRE V  
REstricted Course. Approval Required. Contact Department.

**EENG 5950  MASTER'S THESIS**
001 (8398) CRE V  
REstricted Course. Approval Required. Contact Department.
002 (8423) CRE V  
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003 (8472) CRE V  
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